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*Introduction To FPGA And CPLD – Lesson 10: VHDL vs Verilog vs Schematic*

## FORMULAS

The following formulas and information are meant to go with the online lesson found here:  
[http://www.pyroelectro.com/edu/fpga/vhdl\\_verilog\\_schematic/](http://www.pyroelectro.com/edu/fpga/vhdl_verilog_schematic/)

## LESSON10 VERILOG/VHDL/SCHEMATIC CODE



## ADDITIONAL INFORMATION

If you have any questions about the formulas or information found in this document, please feel free to head on over to the forums and ask us some questions!

<http://www.pyroelectro.com/forums/viewforum.php?f=26>