



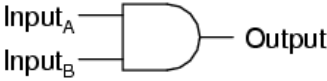
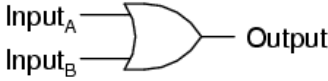
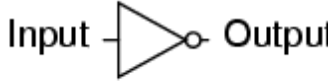
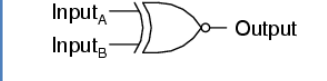
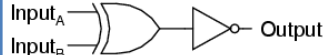
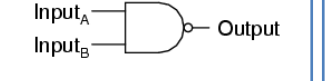
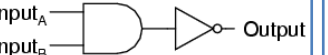

# PyroElectro.com - PyroEDU

## Introduction To Digital Electronics – Lesson 3: Logic Gates

### FORMULAS

The following formulas and information are meant to go with the online lesson found here:  
[http://www.pyroelectro.com/edu/digital/logic\\_gates/](http://www.pyroelectro.com/edu/digital/logic_gates/)

### LOGIC GATE TRUTH TABLES

<p><i>2-input AND gate</i></p>  <table border="1"><thead><tr><th>A</th><th>B</th><th>Output</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></tbody></table>	A	B	Output	0	0	0	0	1	0	1	0	0	1	1	1	<p><i>2-input OR gate</i></p>  <table border="1"><thead><tr><th>A</th><th>B</th><th>Output</th></tr></thead><tbody><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></tbody></table>	A	B	Output	0	0	0	0	1	1	1	0	1	1	1	1	<p><i>NOT gate truth table</i></p>  <table border="1"><thead><tr><th>Input</th><th>Output</th></tr></thead><tbody><tr><td>0</td><td>1</td></tr><tr><td>1</td><td>0</td></tr></tbody></table>	Input	Output	0	1	1	0									
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These are the logic elements we explored in this lesson and their associated truth tables. The NAND and NOR logic gates are also included for completeness sake.

### ADDITIONAL INFORMATION

If you have any questions about the formulas or information found in this document, please feel free to head on over to the forums and ask us some questions!

<http://www.pyroelectro.com/forums/viewforum.php?f=21>