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Introduction To FPGA And CPLD – Lesson 1: The Introduction

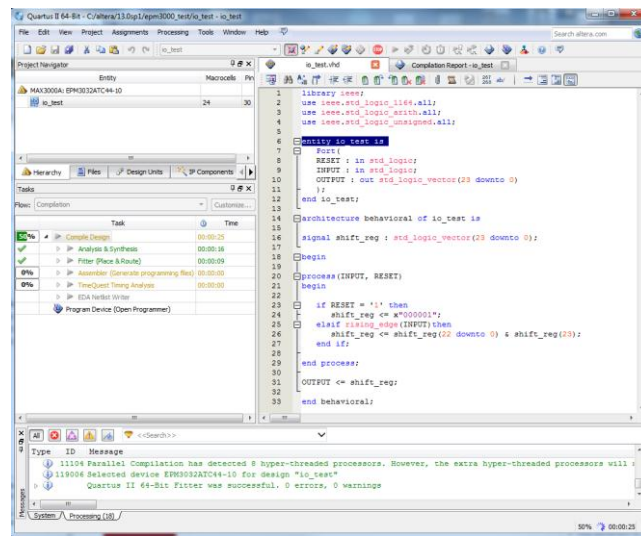
FORMULAS

The following formulas and information are meant to go with the online lesson found here:
<http://www.pyroelectro.com/edu/fpga/introduction/>

EXAMPLE QUARTUS II SETUP

The FPGA/CPLD code that we will be writing for this course will be inside of a program called Quartus II. You can see a screen shot of the program in action to the right.

This is the platform that many FPGA and CPLD developers use to create the hardware images necessary for their projects. Throughout this course you will become very familiar with how Quartus II works, and how to make your own projects in it.



ADDITIONAL INFORMATION

If you have any questions about the formulas or information found in this document, please feel free to head on over to the forums and ask us some questions!
<http://www.pyroelectro.com/forums/viewforum.php?f=26>